

JUN 26 2006

FORM PTO-1449	Docket No.: H35.121.101/12307U5	Serial No.: 10/521,106
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT(S) INFORMATION DISCLOSURE STATEMENT	Applicant: Josef Sock et al.	
	Filing Date: September 13, 2005	Group Art Unit: 2811

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Date	Name	Class	Sub Class	Filing Date if Appropriate
AA						
AB						
AC						
AD						
AE						
AF						
AG						
AH						
AI						
AJ						
AK						
AL						

FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Class	Sub Class	Translated Yes No (Abstract Only)
SC	AM	0 535 350	8/1992	EPO		<input type="checkbox"/> <input checked="" type="checkbox"/>
	AN					<input type="checkbox"/> <input type="checkbox"/>
	AO					<input type="checkbox"/> <input type="checkbox"/>
	AP					<input type="checkbox"/> <input type="checkbox"/>

OTHER ART (Including Author, Title, Date, Pertinent Pages, etc.)

SC	AQ	Ning, Tak H., "Self-Aligned Bipolar Transistor for High-Performance and Low-Power-Delay VLSI," IEEE Transactions on Electron Devices, Vol. ED-28, No. 9, pp. 1010-1013 (September 1981).
	AR	

EXAMINER: /Sara Crane/	DATE CONSIDERED: 08/21/2006
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.